



IC1, 2 = TA7558S (dual SIP OpAmp)
 IC3 = D4027BC (CMOS Dual JK flip-flop)

Q1,2,3,8,9,10,11 = 2SC1815 (NPN)
 Q4,5,6,7 = J304 (2N4416\2N5245 N channel FET)

SCO-1(PCC001000)
 REVISED: 03,MAR,2002

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| TITLE STEREO COMPRESSOR SCO-1 | DRAWING NO. PCC001000 | UENO KAIHATSU CENTER LTD., DRAWING BY Y. SHIBAGAKI DATE 85-4-26 CHK'D BY Y. SHIBAGAKI DATE 85-4-26 DESIGNED BY Y. SHIBAGAKI |
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