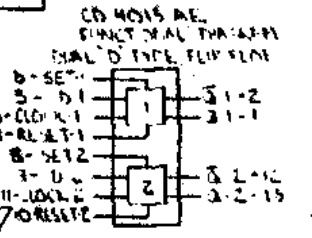
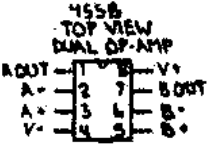
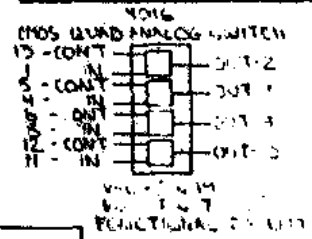


REV	DATE	BY	CHKD
1	5-2-77	W. J.

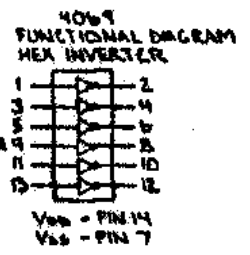


4015
TOP VIEW
ANALOG COMPARE

1	10	11	12	13	14	15	16
1	2	3	4	5	6	7	8

4015
TOP VIEW
ANALOG DELAY

1	2	3	4	5	6	7	8
1	2	3	4	5	6	7	8



VOLTAHAGES		DATE	
SCHEMATIC DIAGRAM - ANALOG DELAY		5-2-77	
1184001-102		...	

T.P. - TEST POINT
T.M. - TRIM POT

TO TRANSMISSION
GATES
-TG-