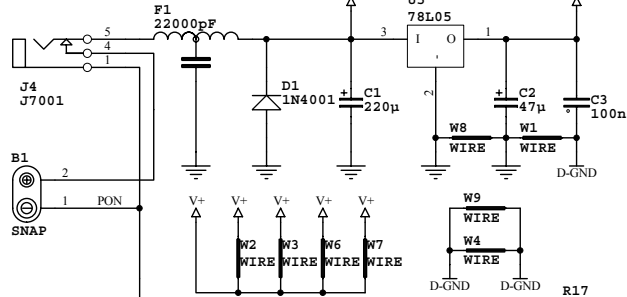
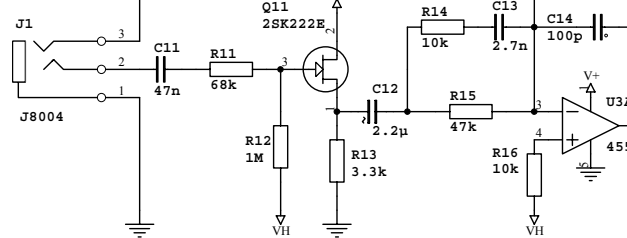


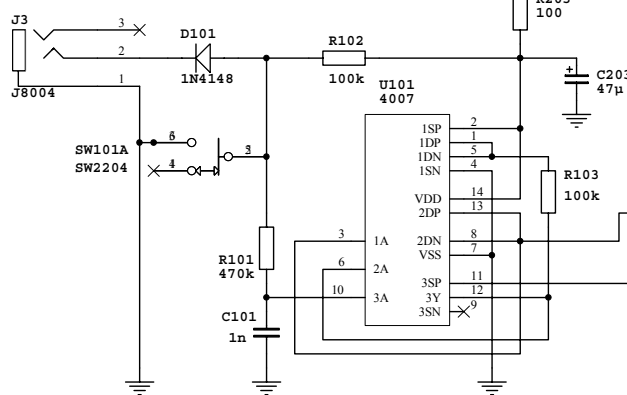
EXTERNAL POWER SUPPLY



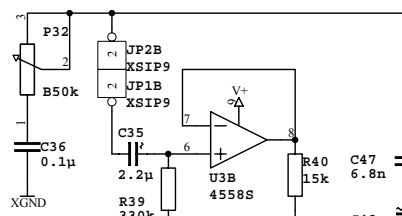
INPUT



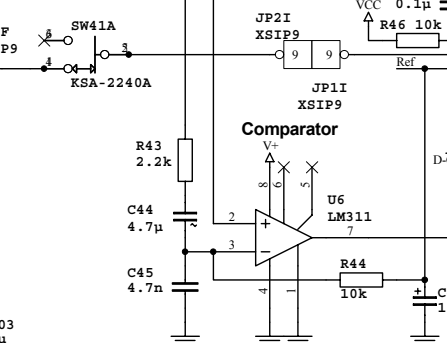
REMOTE



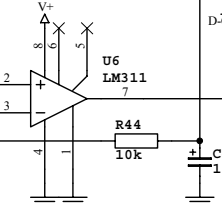
Tone



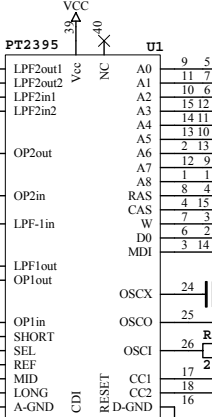
Short / Long - Delay



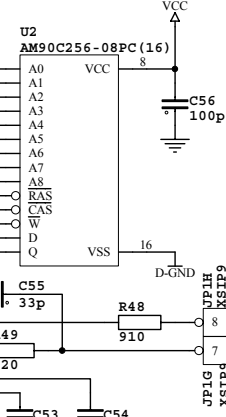
Comparator



Signal Processor



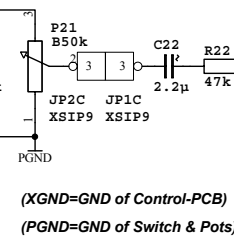
DRAM 256



Reset

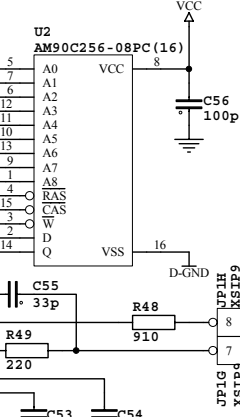






Delay Level



(XGND=GND of Control-PCB)
(PGND=GND of Switch & Pots)

Repeat



- Mylar Capacitor 
- Ceramic Capacitor 
- Non Polar Electrolytic Capacitor 
- Std. Electrolytic Capacitor 

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NOBELS ELECTRONICS			
Barnerstrasse 42, 22765 Hamburg, Germany www.nobel.com - email: info@nobels.com			
Title: CIRCUIT DIAGRAM			
No.:	Digital Delay / DD-800	Size:	A4
Date:	15-Apr-2003	Time:	18:52:32
File:	DD-800 R4a.sch	Sheet:	1 of 1